

# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY, HYDERABAD – 500 085

EXAMINATION BRANCH

II YEAR B.TECH - I SEMESTER – R18 REGULATION REGULAR/SUPPLEMENTARY EXAMINATIONS APRIL/MAY -2023

TIME TABLE

TIME → FN: 10:00 AM TO 1:00 PM

| BRANCH                                                                                                         | DATE, SESSION AND DAY                          |                                            |                                             |                       |                                            |                                              |
|----------------------------------------------------------------------------------------------------------------|------------------------------------------------|--------------------------------------------|---------------------------------------------|-----------------------|--------------------------------------------|----------------------------------------------|
|                                                                                                                | 17-04-2023<br>MONDAY                           | 19-04-2023<br>WEDNESDAY                    | 21-04-2023<br>FRIDAY                        | 25-04-2023<br>TUESDAY | 27-04-2023<br>THURSDAY                     | 29-04-2023<br>SATURDAY                       |
| ELECTRONICS<br>&<br>COMMUNICATIONS<br>ENGINEERING<br>(04- ECE)                                                 | Probability Theory and<br>Stochastic Processes | Network Analysis and<br>Transmission Lines | Digital System Design                       | Signals and Systems   | Electronic Devices and<br>Circuits         | ---                                          |
| COMPUTER SCIENCE<br>AND ENGINEERING<br>(ARTIFICIAL<br>INTELLIGENCE AND<br>MACHINE LEARNING)<br>(66-CSE(AI&ML)) | Discrete Mathematics                           | Data Structures                            | Mathematical and<br>Statistical Foundations | Python Programming    | Business Economics &<br>Financial Analysis | Computer<br>Organization and<br>Architecture |